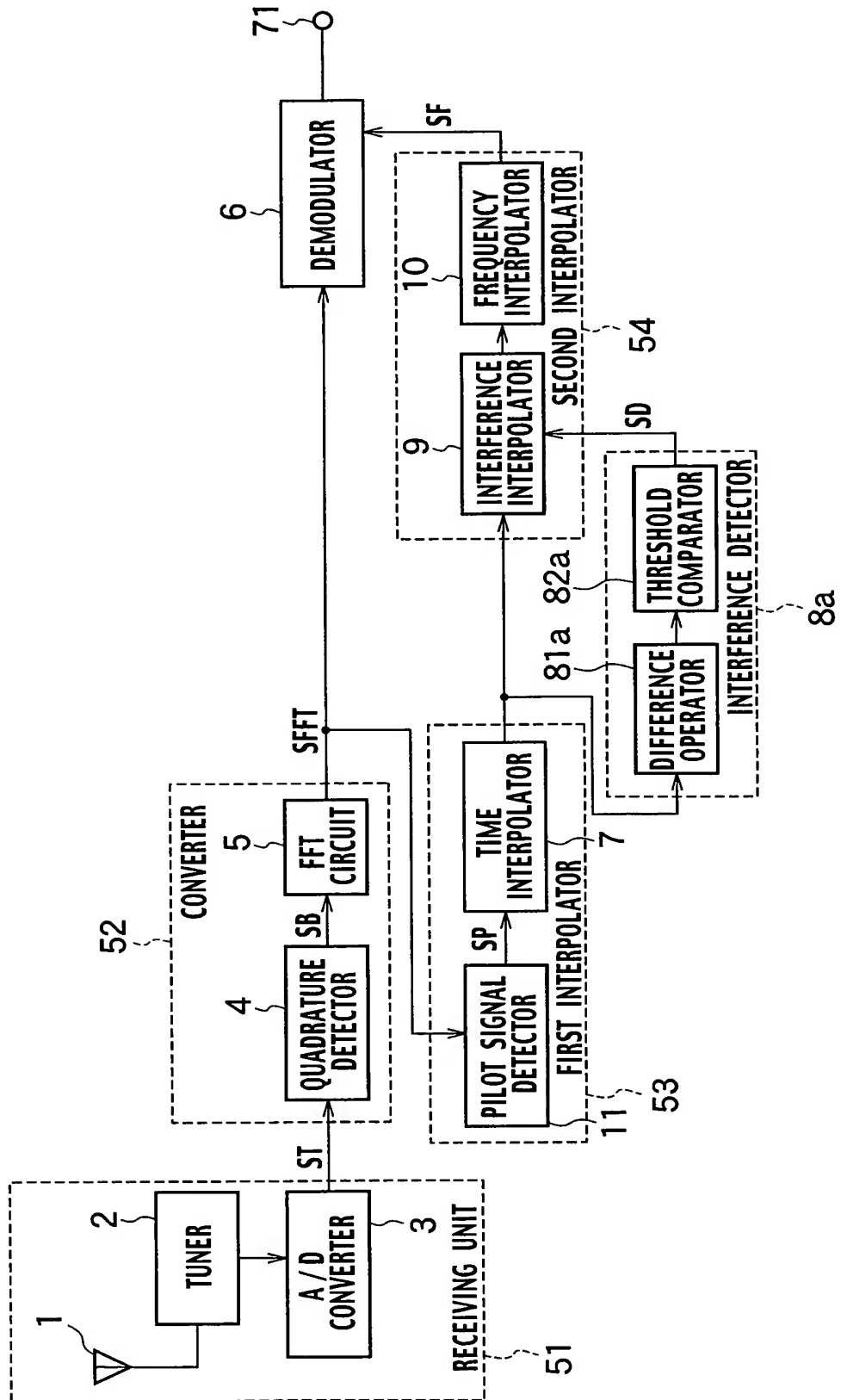


FIG. 1



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FIG. 2

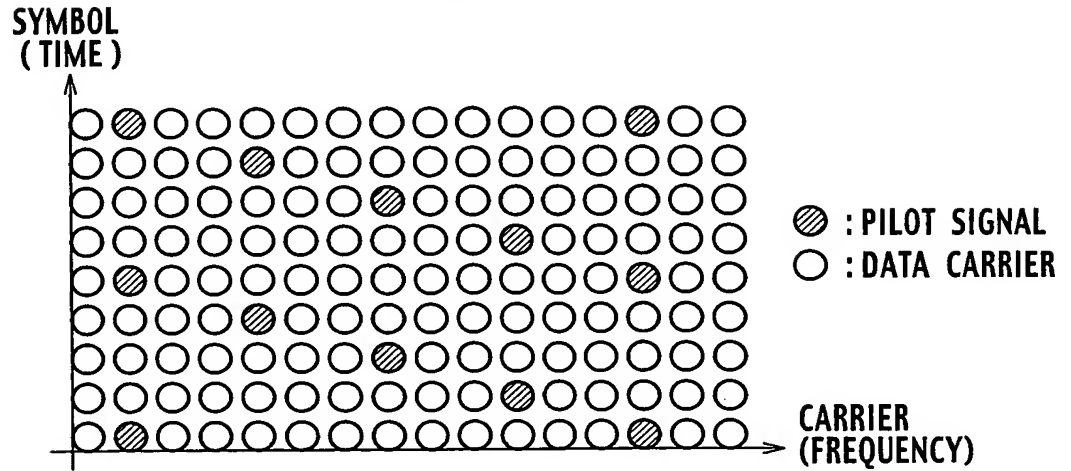


FIG. 3

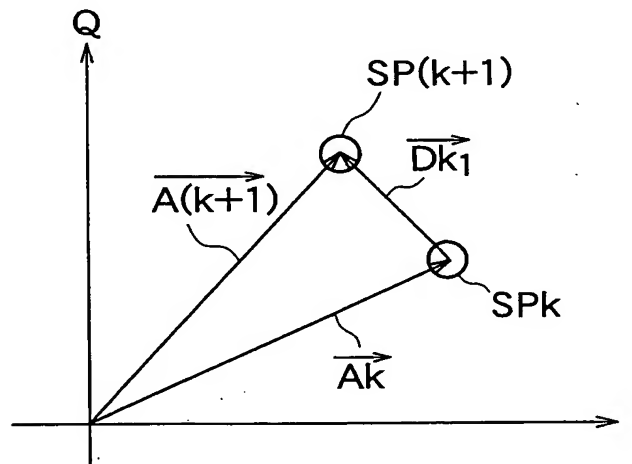
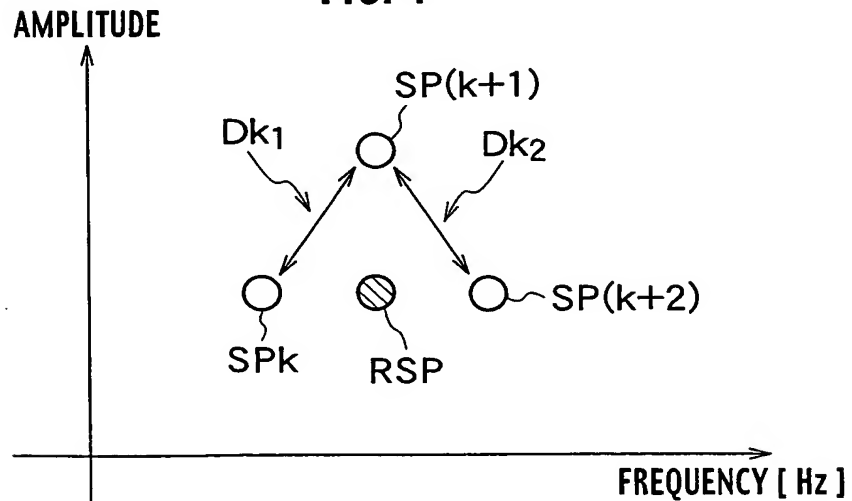
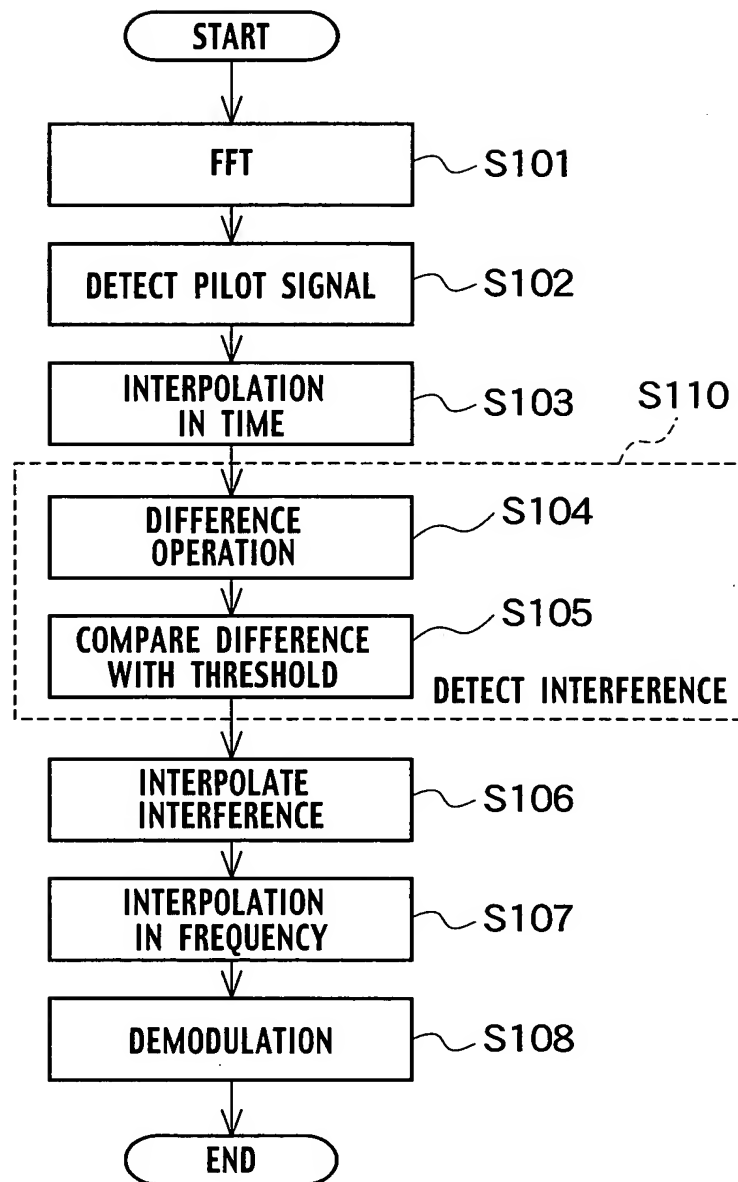


FIG. 4



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FIG. 5



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FIG. 6

92 SEMICONDUCTOR INTEGRATED CIRCUIT

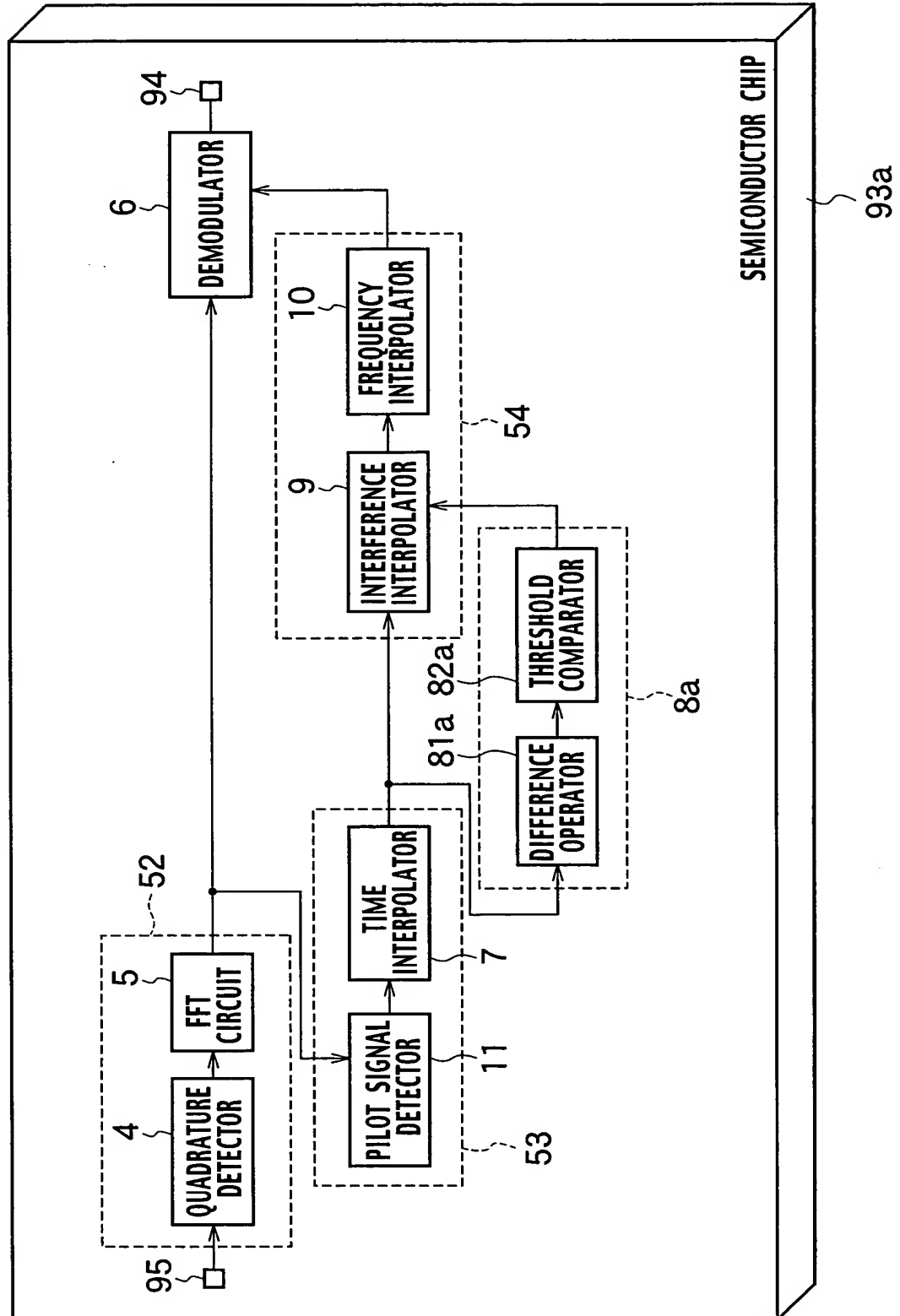
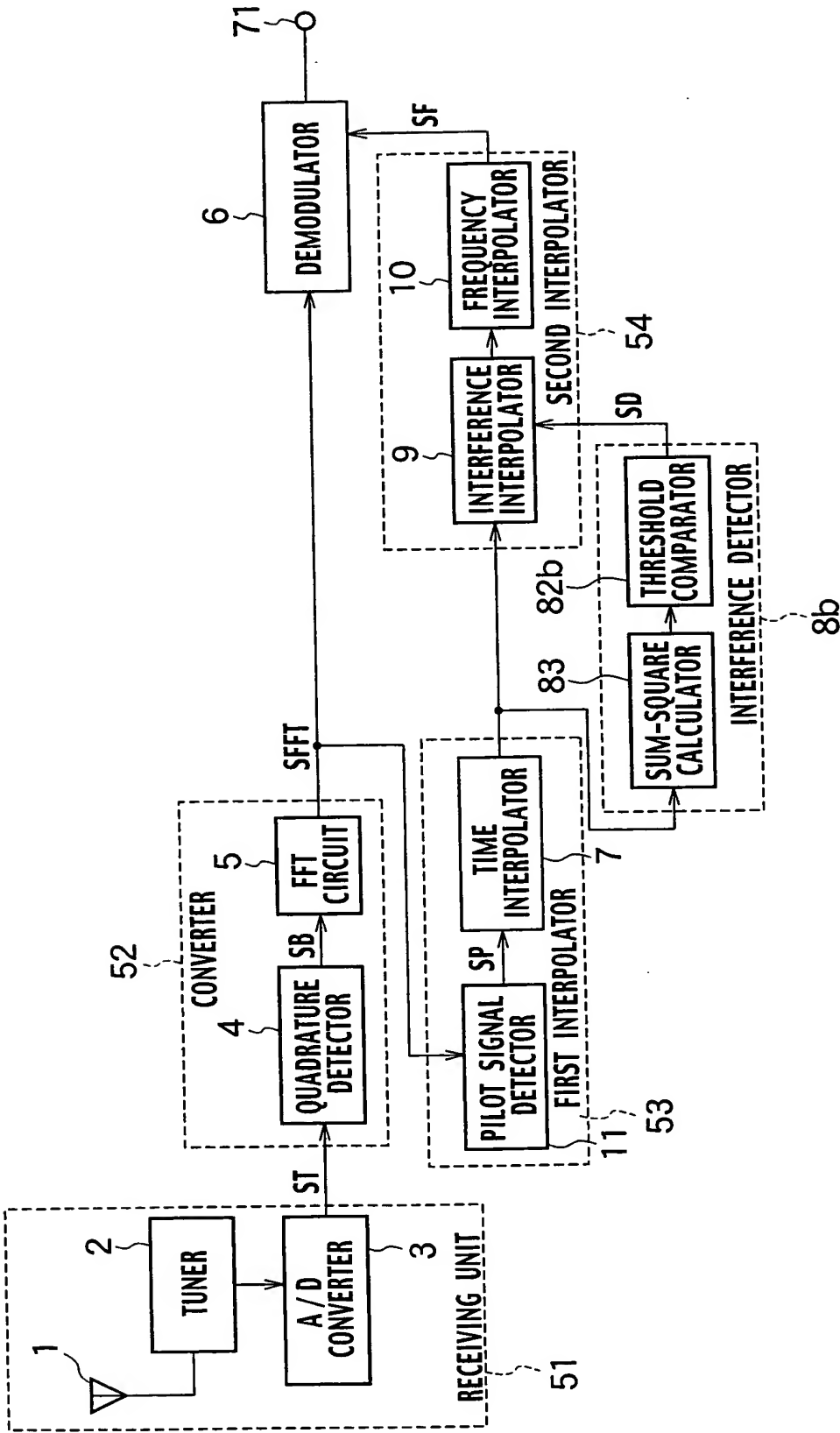
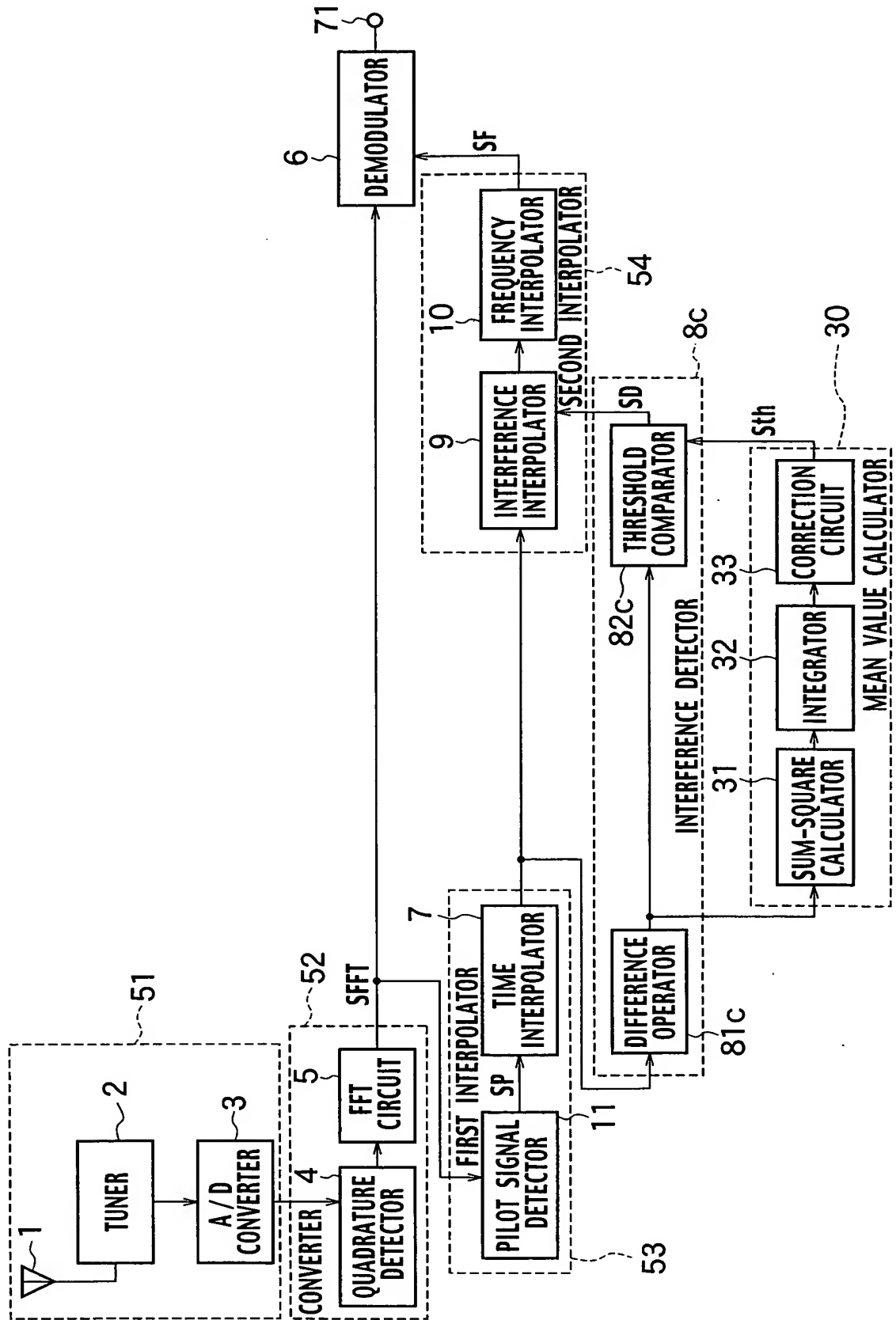


FIG. 7



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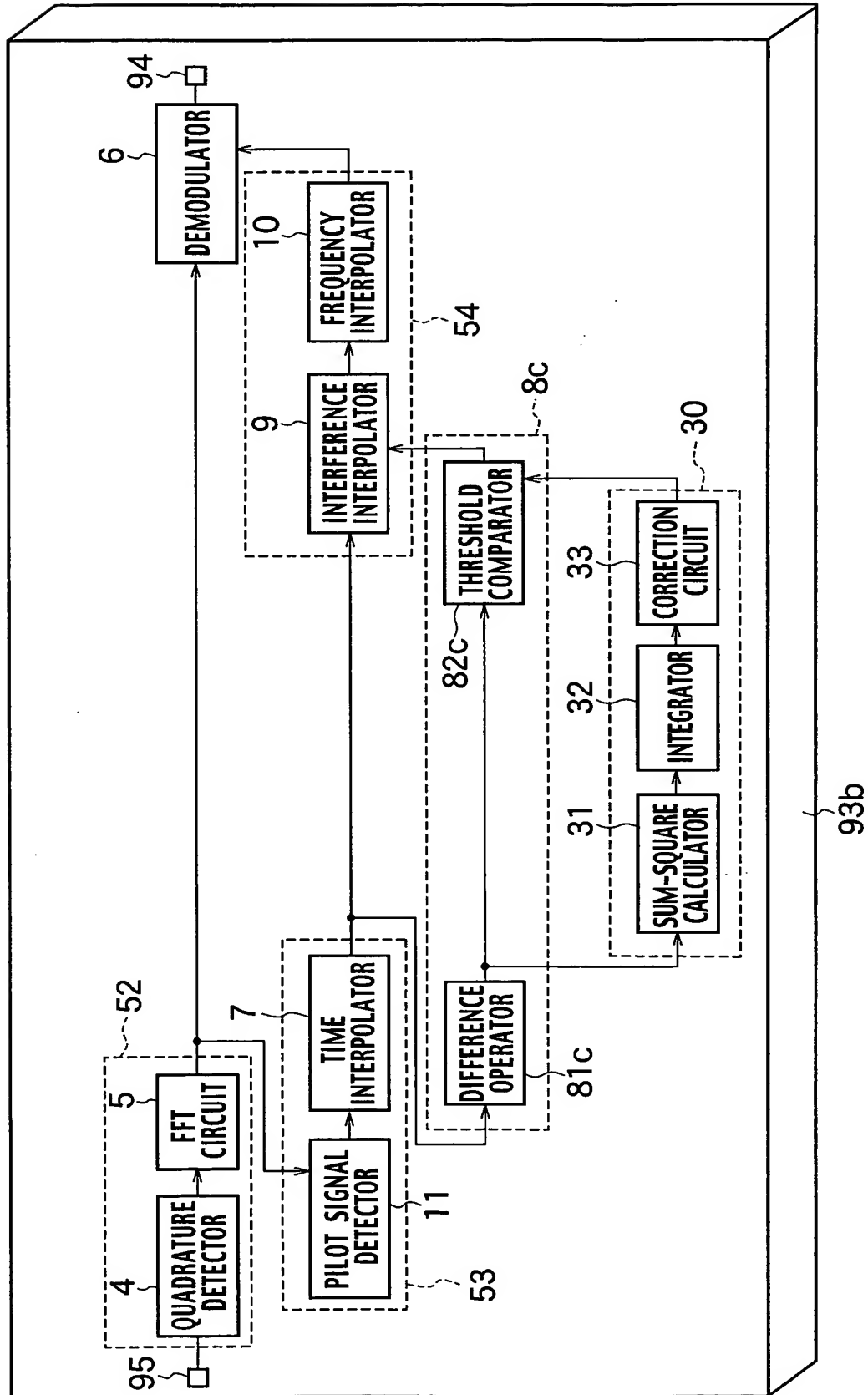
FIG. 8



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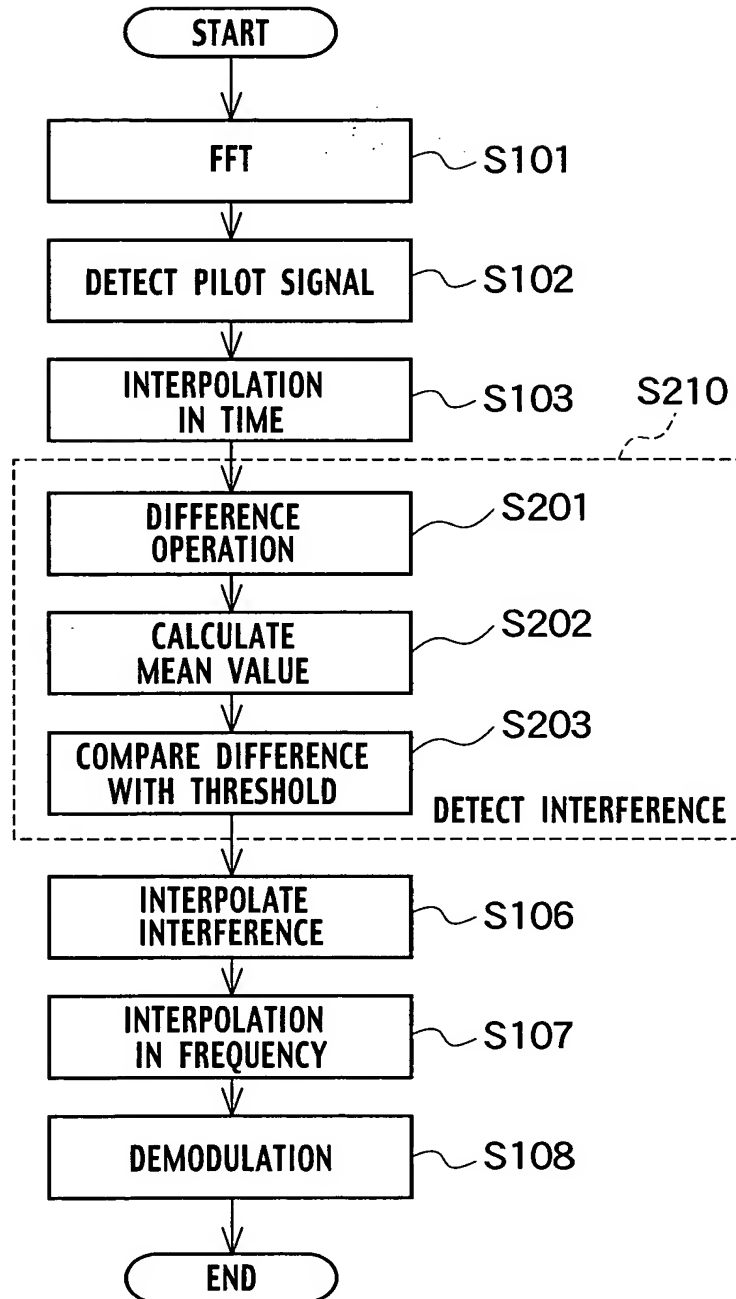
FIG. 9

96 SEMICONDUCTOR INTEGRATED CIRCUIT



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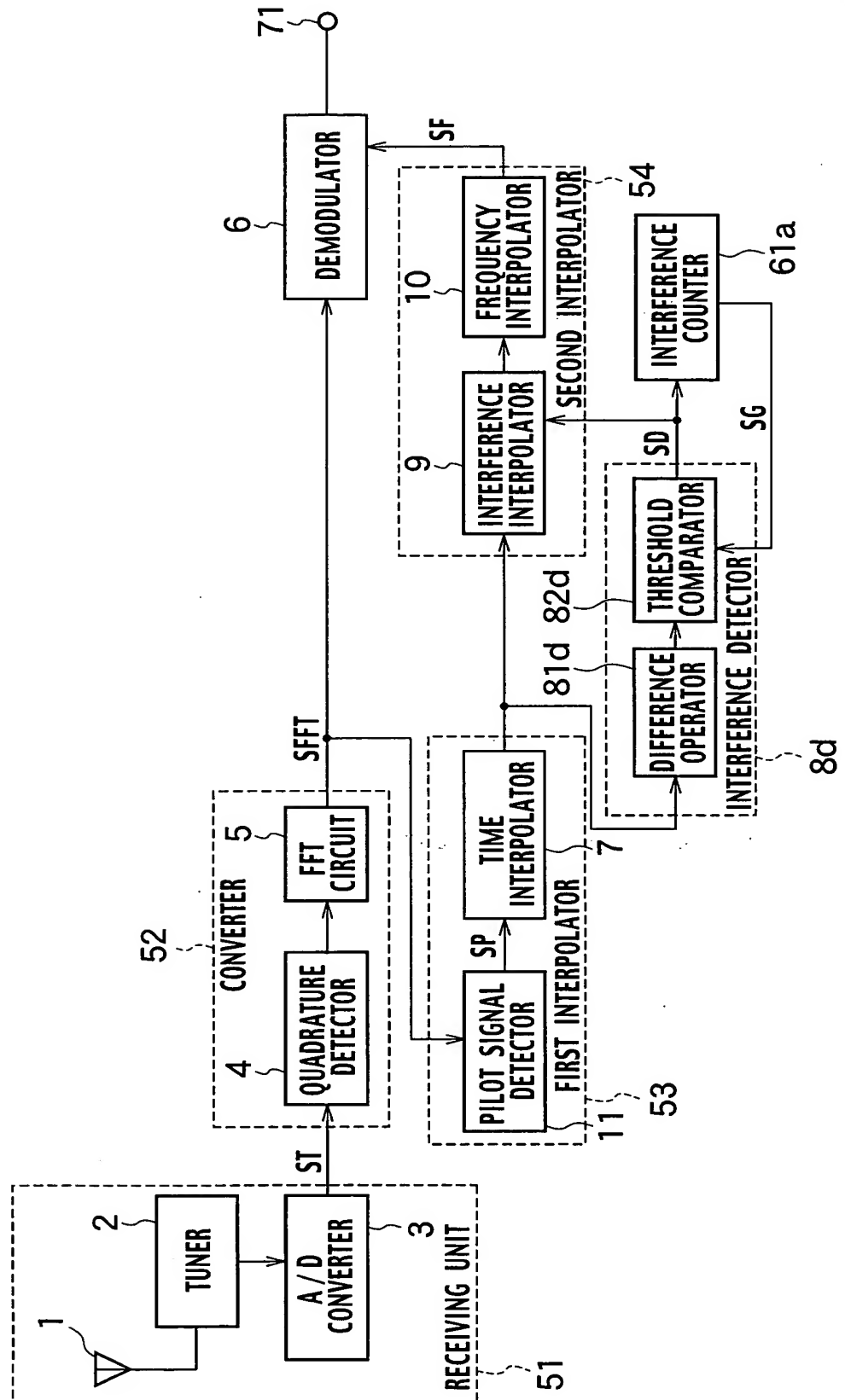
FIG. 10





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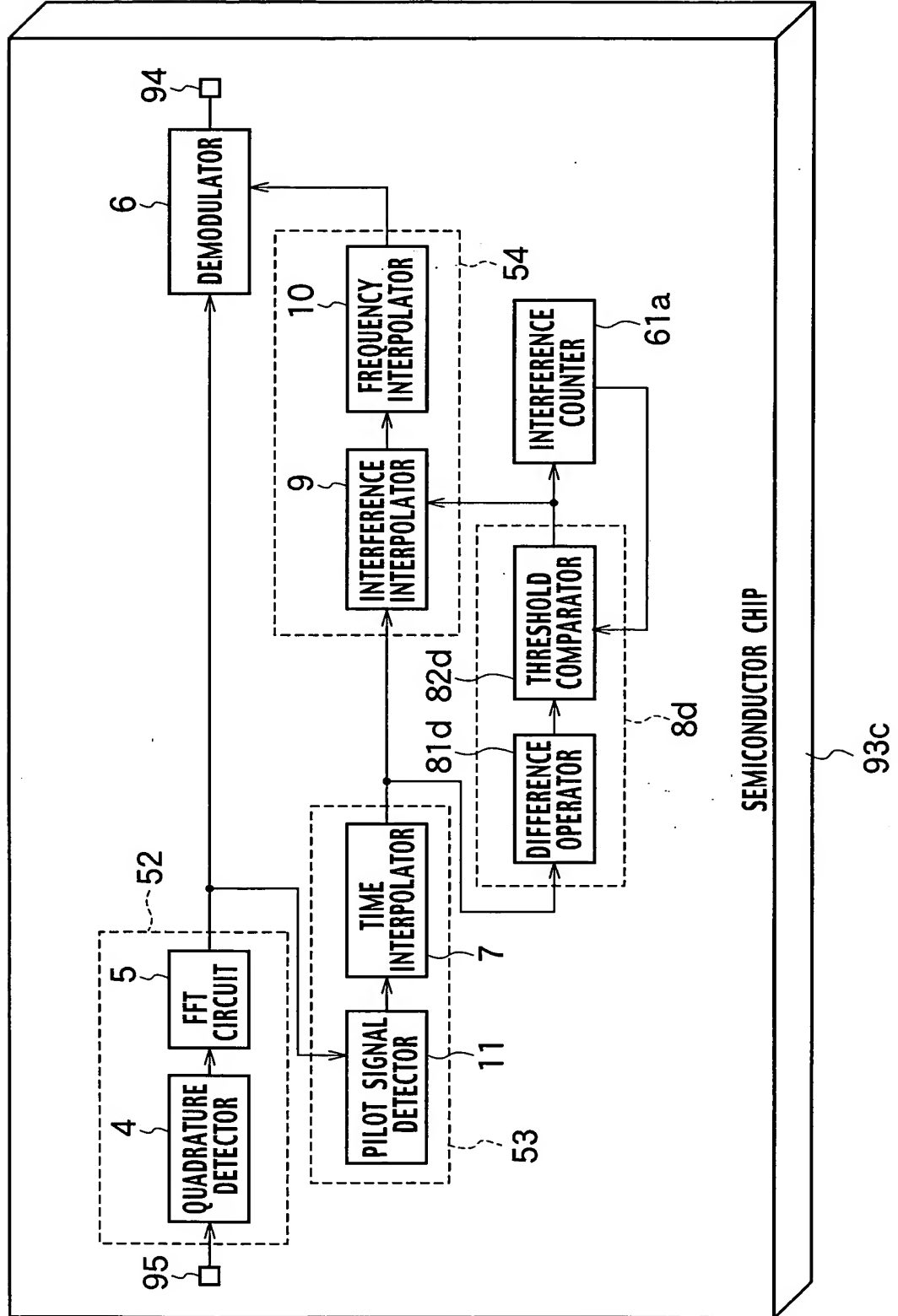
FIG. 11



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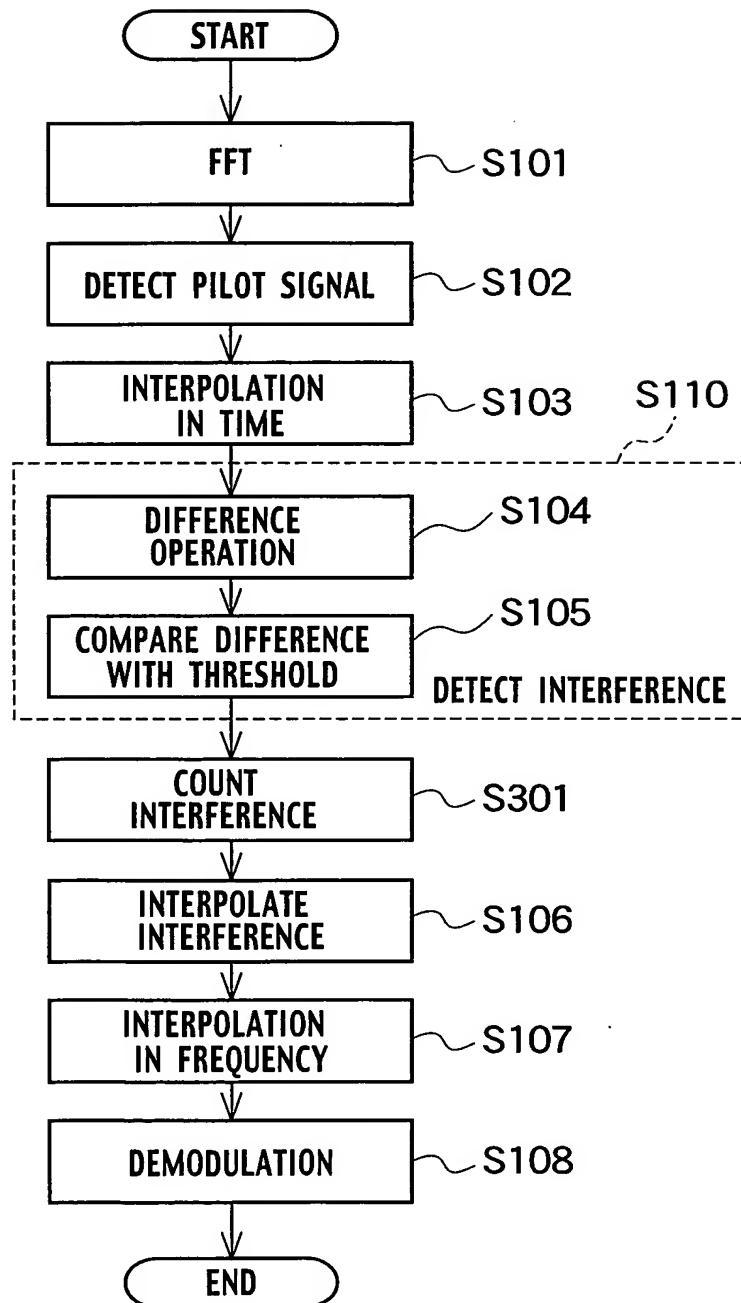
FIG. 12

97 SEMICONDUCTOR INTEGRATED CIRCUIT



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FIG. 13



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FIG. 14

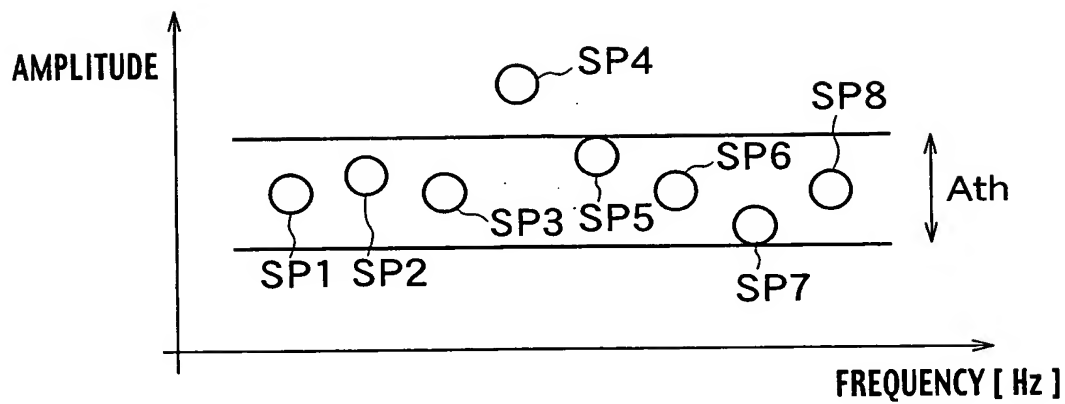
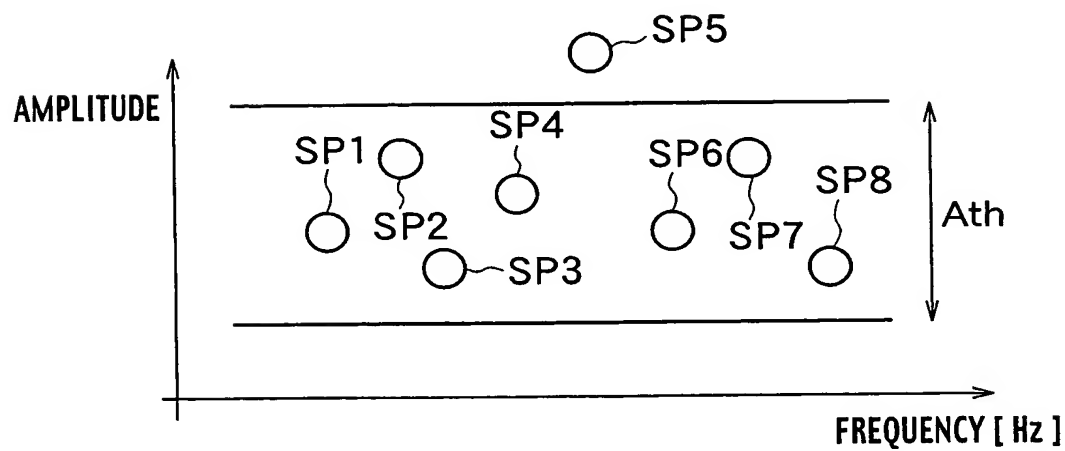


FIG. 15



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FIG. 16

